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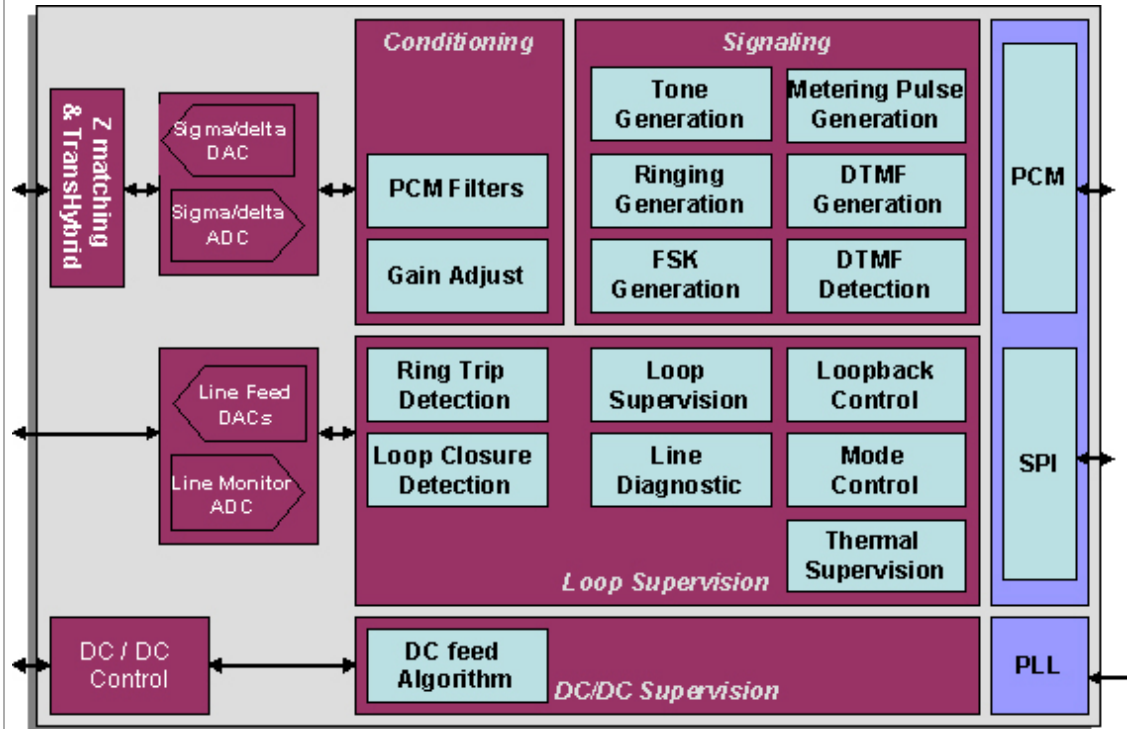
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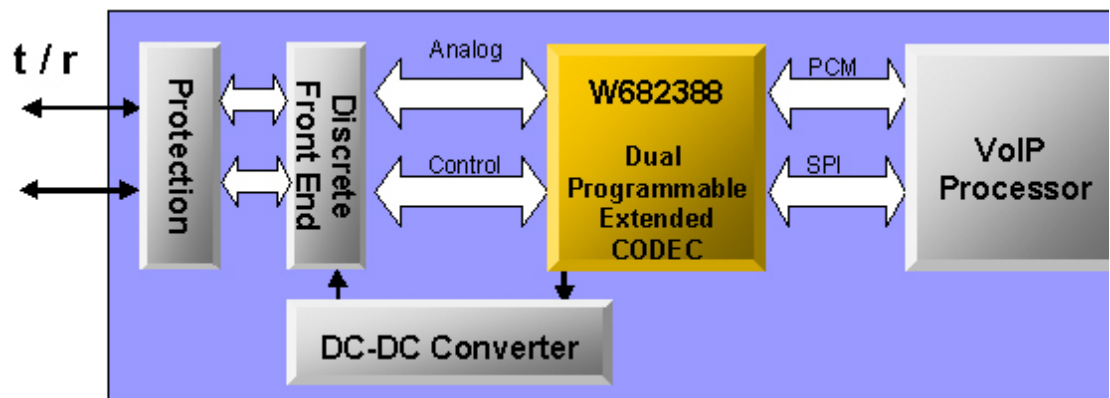
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## Pro-X CODEC/SLCC

<b>Part No.</b>	<b>W682388</b>
<b>Datasheet</b>	<a href="#">Request PDF</a>
<b>Description</b>	<p>The W682388 implements a dual channel FXS telephone line interface optimized for short loop application dual SLCC (Subscriber Line Control Circuit) functionality with a dual channel programmable CODEC. The supports internal ringing up to <math>90V_{PK}</math> (5REN at 4kft) ideal for customer premise equipment (CPE). The Ct configured for <math>\mu</math>-law/A-law or 16-bit linear PCM encoding and supports a comprehensive set of signaling required to supervise and control both telephone lines. These include tone generation, including ring tone generation and detection as well as FSK and metering pulse generation. An on-chip dual pulse width mod driver allows the control of DCDC conversion for each line. Programmable impedance and transhybrid ba for worldwide deployment.</p> <p>For additional technical documentation please <a href="#">Contact Sales</a>.</p>
<b>Diagram</b>	<p><a href="#">Block Diagram</a></p>



Application Diagram



Features

- Performs complete BORSCHT functions for two channels on a single device
- Internal balanced ringing up to 90 V<sub>PK</sub> (5 REN up to 4 kft)
- Integrated Power Management
  - Integrated PWM controls line voltage to minimize power dissipation in all operating modes
- Programmable linefeed characteristics
  - Ringing frequency, amplitude, cadence
  - Trapezoidal and Sinusoidal waveforms
  - Two-wire AC impedance, transhybrid balance
  - Constant Current feed (20 to 41 mA)
  - Ring trip and loop closure threshold
- Loop test and diagnostics support
  - Integrated loopback modes
  - Real-time linefeed monitor
- Programmable signal generation
  - DTMF generation and detection
  - Metering pulse generation (12/16 kHz)
  - Caller ID (FSK) generation
  - Tone generation (up to four simultaneous)

	G.711 $\mu$ -Law/A-Law / 16-bit linear PCM audio Programmable audio path gains PCM and SPI bus digital interfaces On-chip PLL for flexible clocking options 3.3V operation Small Footprint Packages:LQFP-64, QFN-64	
<b>Package</b>	LQFP-64, QFN-64	
<b>Other Files</b>	N/A	
<b>Development Tools</b>	Evaluation System:	
<b>Others</b>	<b>Parametric</b>	
	<b>PART NUMBER</b>	<b>DESCRIPTION</b>
	<b>W682388DG</b>	Pb-free, dual channel FXS telephone line interface, $\mu$ -law/A-law or 16-bit linear PCM
	<b>W682388YG</b>	Pb-free, dual channel FXS telephone line interface, $\mu$ -law/A-law or 16-bit linear PCM

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